

# CHIPOLA COLLEGE

#### COURSE SYLLABUS

Chipola's website: www.chipola.edu

COURSE TITLE: COURSE NUMBER:

Introduction to Electronics

**EET 1084C** 

#### **COURSE DESCRIPTION (with prerequisites):**

This course provides the foundation for electronic circuits and measurements. Students will study principles of electricity, magnetism, and basic laws of electronics. Course topics will include fundamentals of DC circuits, AC circuits, semiconductors, and digital circuits. The course will integrate the conceptual measurements with the various electronic measuring instruments and their use in weekly laboratory exercises. 3 semester credit hours. (5 contact hours)

#### NAME(S) OF INSTRUCTORS:

TBA

#### **EFFECTIVE ACADEMIC YEAR:**

2023-2024

#### REQUIRED TEXTBOOKS AND INSTRUCTIONAL MATERIALS:

Introduction to Electronics by Earl Gates, 6th edition, ISBN-13: 978-1111128531

Amatrol eBook, 5085-1C Advanced Manufacturing Program Summary, Volume 1, Copyright 2013 Amatrol, Inc.

#### **GRADING POLICY:**

The standing of a student in each course is expressed by one of the following letters and corresponding grading system:

A - 90 - 100

B - 80 - 89

C - 70 - 79

D - 60 - 69

**F – 59 or less** 

The Chipola Catalog provides policies and procedures regarding the grading system. A student's Grade Point Average is derived from the grading system/quality point scale.

#### ATTENDANCE AND WITHDRAWAL POLICIES:

Chipola College expects regular attendance of all students, and all instructors record attendance daily. Students who are absent from classes for any reason other than official college activities must satisfy the instructor concerned that the absence was due to illness or other clearly unavoidable reasons. Otherwise, the student may suffer grade loss at the discretion of the instructor. Chipola policy allows each instructor to specify in

the Instructor First Day Handout whether or not an absence is excusable and what affect the absence or tardy may have on the grade.

A student is allowed to repeat a course a maximum of three (3) times. On the third attempt a student (1) must bear the full cost of instruction (unless waived by Student Services), (2) cannot withdraw, and (3) must receive a grade.

#### **MAKE-UP POLICY:**

Chipola allows each instructor to specify in the Instructor First Day Handout the makeup policy.

#### **ACADEMIC HONOR CODE POLICY:**

Students are expected to uphold the Academic Honor Code. Chipola College's Honor Code is based on the premise that each student has the responsibility to (1) uphold the highest standards of academic honesty in his/her own work; (2) refuse to tolerate academic dishonesty in the college community; and (3) foster a high sense of honor and social responsibility on the part of students. Further information regarding the Academic Honor Code may be found in the Chipola Catalog, Student Governance section.

#### STUDENTS WITH DISABILITIES POLICY:

Chipola College is committed to making all programs and facilities accessible to anyone with a disability. Chipola's goal is for students to obtain maximum benefit from their educational experience and to effectively transition into the college environment. Students with disabilities are requested to voluntarily contact the Office of Students with Disabilities to complete the intake process and determine their eligibility for reasonable accommodations.

### NOTICE OF EQUAL ACCESS/EQUAL OPPORTUNITY AND NONDISCRIMINATION:

Chipola College does not discriminate against any persons, employees, students, applicants or others affiliated with the college with regard to race, color, religion, ethnicity, national origin, age, veteran's status, disability, gender, genetic information, marital status, pregnancy or any other protected class under applicable federal and state laws, in any college program, activity or employment.

Wendy Pippen, Associate Vice President of Human Resources, Equity Officer and Title IX Coordinator, 3094 Indian Circle, Marianna, FL 32446, Building A, Room 183C, 850-718-2269, pippenw@chipola.edu.

#### LIBRARY AND ON-LINE REFERENCE MATERIALS:

The library is a comprehensive learning resource center providing information in print, electronic, and multimedia format to support the educational objectives of the College. On-line catalogs, e-books and electronic databases can be accessed by using the *LINCCWeb* icon on the Chipola Library website at <a href="www.chipola.edu/library">www.chipola.edu/library</a>. If you have questions about database usage consult the "How to Use the Chipola Databases" on the Library website or call the Library at 850/718-2274 during regular hours. Library hours are posted each semester at the building entrance and on the Library website.

See your Instructor First Day Handout for individual instructor recommendations and resources.

#### **TECHNOLOGY RESOURCES:**

The college's learning management system is **Canvas**. Classes become available on Canvas on the first day of the semester. It is the student's responsibility to log onto the Canvas system the first day of class to establish the first day of attendance and to check announcements. All official class communication must be through Canvas. For further information, contact your instructor or the Director of eLearning. The Canvas support hotline is available online in live chat and on the phone, toll-free, at 855-308-2812 for any issues in accessing or utilizing Canvas. The **Technology Center**, located in the library, is equipped with computer workstations. Lab hours are posted each semester at the building entrance and on the Library website.

#### FREE TUTORING RESOURCES:

The Academic Center for Excellence (ACE) Lab, located in Building L, offers free tutoring from 8 a.m. to 5 p.m. and is equipped with computer workstations. ACE Lab hours are posted each semester at the room entrance and on the website. Additionally, live online tutoring conferences and individual tutoring sessions are available for a variety of courses through ACE @ Home. For a conference schedule or to schedule an individual appointment, visit "ACE Tutoring" in the left navigation from any course in Canvas.

#### **ELECTRONIC DEVICE USAGE STATEMENT:**

Classrooms should be free of all unnecessary distractions from the task of learning. Therefore, as a general rule, students should silence and avoid use of all electronic devices (laptops, phones, tablets, etc.) not being used for coursework. Consult first-day handouts for any specific policies related to the use of electronic devices in the classroom, as they may vary depending upon the nature of the course or the guidelines of the instructor. Faculty reserve the right to regulate the use of electronic devices and their accessories in class.

#### **DISCIPLINE SPECIFIC COMPETENCIES / LEARNING OUTCOMES:**

This course provides an introduction to the basic fundamentals, terminology, and applications used in the electronics industry. The topic coverage will include circuit theory principles, electronic components, transistor usage, amplifiers, power supplies, digital logic techniques, and electronic instruments. Topics include: fundamentals of electricity, current, voltage, resistance, Ohm's law, electrical measurements, power, DC circuits, magnetism, inductance, capacitance, AC circuits, transformers, semiconductors, integrated circuits, optoelectric devices, power supplies, amplifiers, oscillators, binary number system, basic logic gates and simplification, printed circuit board fabrication and repair, electronic troubleshooting techniques, etc.

# LINKING COURSE-LEVEL STUDENT LEARNING OUTCOMES WITH DISCIPLINE-SPECIFIC COMPETENCIES, ASSESSMENT METHODS, AND ARTIFACTS

COURSE-LEVEL STUDENT LEARNING OUTCOMES FOR EET 1084C

DISCIPLINE-SPECIFIC GENERAL EDUCATION COMPETENCIES ASSESSMENT METHODS FOR COURSE LEVEL STUDENT LEARNING OUTCOMES

LEARNING ARTIFACTS FOR AA PROGRAM ASSESSMENT Upon the successful completion of this course, students will be able to:

- Understand electricity and electronics
- Work with electronic components, circuits and devices
- 3. Understand AC and DC electric circuit theory
- Analyze circuits and utilize circuit models to solve electrical problems
- Apply circuit techniques to semiconductors and discrete components
- 6. Utilize measuring instruments and electronic workstations
- 7. Use computer simulation techniques to analyze electronic circuits

Demonstrate an understanding of industrial processes and material properties.

Demonstrate ability to generate and interpret computer-aided drawings (CAD).

Demonstrate a fundamental understanding of electronics and electricity.

Demonstrate an understanding of industrial safety, health, and environmental requirements.

Demonstrate proficiency in the use of quality assurance methods and quality control concepts.

Demonstrate proficiency in using tools, instruments, and testing devices.

Demonstrate math skills appropriate for employees in an engineering work environment.

Demonstrate basic troubleshooting skills appropriate for employees in an engineering work environment. Assessment methods used are:

Q, T, CF, SD, SP, H, Sk Check

**Assessment Codes			
T = Tests	RPT = Report/Presentation	Proj. = Projects	<b>BO</b> = Behavioral Observation
<b>Pre/Post</b> = Pre- and Post-Tests	<b>SP</b> = Skills Performance	Exp. = Experiments	Clin. = Clinicals
<b>OT</b> = Objective Tests	<b>SD</b> = Skills Demonstration	Cap. Proj. = Capstone Project	CS = Case Study
UT = Unit Tests	<b>W</b> = Writing Assignments	Cap. Course = Capstone	CP = Case Plan
<b>Q</b> = Quizzes	<b>E</b> = Essays	Course	Port. = Portfolio
<b>F</b> = Final Examination	<b>DE</b> = Documented Essays	Prac. = Practicum	Obs. = Teacher Observation
CF = Cumulative Final	RP = Research papers	Intern. = Internship	Sk. Check = Skills Check-off
EX = Departmental Exam	<b>J</b> = Jury	<b>H</b> = Homework	Curriculum Frameworks
SE = Nat'l or State Standardized	R = Recital	<b>PS</b> = Problem Solving	JP = Judged
Exam		<b>DB</b> = Discussion Board	Performance/Exhibition

## MEANS OF ACCOMPLISHING STUDENT LEARNING OUTCOMES:

- 1. In-class lecture and discussion.
- 2. Hands-on competency-based labs.
- 3. Assessments.

ASSIGNMENT AND/OR COURSE OUTLINE
See your Instructor First Day Handout for individual instructor assignment schedule.